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# EmQ-i2200

**Qseven® CPU Module**

## **User's Manual**

**Version 1.0**

2016.05



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## Revision History

Version	Release Time	Description
1.0	May, 2016	Initial release

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## Copyright Notice

All Rights Reserved.

The information in this document is subject to change without prior notice in order to improve the reliability, design and function. It does not represent a commitment on the part of the manufacturer.

Under no circumstances will the manufacturer be liable for any direct, indirect, special, incidental, or consequential damages arising from the use or inability to use the product or documentation, even if advised of the possibility of such damages.

This document contains proprietary information protected by copyright. All rights are reserved. No part of this manual may be reproduced by any mechanical, electronic, or other means in any form without prior written permission of the manufacturer.

## Declaration of Conformity CE

The CE symbol on your product indicates that it is in compliance with the directives of the Union European (EU). A Certificate of Compliance is available by contacting Technical Support.

This product has passed the CE test for environmental specifications when shielded cables are used for external wiring. We recommend the use of shielded cables. This kind of cable is available from ARBOR. Please contact your local supplier for ordering information.

This product has passed the CE test for environmental specifications. Test conditions for passing included the equipment being operated within an industrial enclosure. In order to protect the product from being damaged by ESD (Electrostatic Discharge) and EMI leakage, we strongly recommend the use of CE-compliant industrial enclosure products.

### **Warning**

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

## **FCC Class A**

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference, and
- (2) This device must accept any interference received, including interference that may cause undesired operation.

### **NOTE:**

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

## **RoHS**

ARBOR Technology Corp. certifies that all components in its products are in compliance and conform to the European Union's Restriction of Use of Hazardous Substances in Electrical and Electronic Equipment (RoHS) Directive 2002/95/EC.

The above mentioned directive was published on 2/13/2003. The main purpose of the directive is to prohibit the use of lead, mercury, cadmium, hexavalent chromium, polybrominated biphenyls (PBB), and polybrominated diphenyl ethers (PBDE) in electrical and electronic products. Member states of the EU are to enforce by 7/1/2006.

ARBOR Technology Corp. hereby states that the listed products do not contain unintentional additions of lead, mercury, hex chrome, PBB or PBDB that exceed a maximum concentration value of 0.1% by weight or for cadmium exceed 0.01% by weight, per homogenous material. Homogenous material is defined as a substance or mixture of substances with uniform composition (such as solders, resins, plating, etc.). Lead-free solder is used for all terminations (Sn(96-96.5%), Ag(3.0-3.5%) and Cu(0.5%)).

## **SVHC / REACH**

To minimize the environmental impact and take more responsibility to the earth we live, Arbor hereby confirms all products comply with the restriction of SVHC (Substances of Very High Concern) in (EC) 1907/2006 (REACH --Registration, Evaluation, Authorization, and Restriction of Chemicals) regulated by the European Union.

All substances listed in SVHC < 0.1 % by weight (1000 ppm)

## **Warning**

Single Board Computers and their components contain very delicate Integrated Circuits (IC). To protect the Single Board Computer and its components against damage from static electricity, you should always follow the following precautions when handling it:

1. Disconnect your Single Board Computer from the power source when you want to work on the inside.
2. Hold the board by the edges and try not to touch the IC chips, leads or circuitry.
3. Use a grounded wrist strap when handling computer components.
4. Place components on a grounded antistatic pad or on the bag that comes with the Single Board Computer, whenever components are separated from the system.

## **Replacing the Lithium Battery**

Incorrect replacement of the lithium battery may lead to a risk of explosion.

The lithium battery must be replaced with an identical battery or a battery type recommended by the manufacturer.

Do not throw lithium batteries into the trash-can. It must be disposed of in accordance with local regulations concerning special waste.

## **Technical Support**

If you have any technical difficulties, please do not hesitate to call or e-mail our customer service.

<http://arbor-technology.com/>

E-mail: [info@arbor.com.tw](mailto:info@arbor.com.tw)



## Warranty

This product is warranted to be in good working order for a period of two years from the date of purchase. Should this product fail to be in good working order at any time during this period, we will, at our option, replace or repair it at no additional charge except as set forth in the following terms. This warranty does not apply to products damaged by misuse, modifications, accident or disaster.

Vendor assumes no liability for any damages, lost profits, lost savings or any other incidental or consequential damage resulting from the use, misuse of, or inability to use this product. Vendor will not be liable for any claim made by any other related party. Vendors disclaim all other warranties, either expressed or implied, including but not limited to implied warranties of merchantability and fitness for a

particular purpose, with respect to the hardware, the accompanying product's manual(s) and written materials, and any accompanying hardware. This limited warranty gives you specific legal rights.

Return authorization must be obtained from the vendor before returned merchandise will be accepted. Authorization can be obtained by calling or faxing the vendor and requesting a Return Merchandise Authorization (RMA) number. Returned goods should always be accompanied by a clear problem description.

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# Chapter 1

## Introduction

## 1.1. The Product

- Fanless Design
- Soldered Onboard Intel® Atom™ Celeron Processor N3160
- Integrated Gigabit Ethernet
- Dual Channels 24-bit LVDS, 1 x DisplayPort / HDMI selectable port
- Extended Operating Temp.: -20 ~ 70°C

## 1.2. About this Manual

This manual is intended for experienced users and integrators with hardware knowledge of computers. If you are not sure about the description in this manual, consult your vendor before further handling.

We recommend that you keep one copy of this manual for the quick reference for any necessary maintenance in the future. Thank you for choosing ARBOR products.

### 1.3. Specifications

Form Factor	Qseven® CPU Module
CPU	Soldered onboard Intel® Celeron Processor N3160
System Memory	Soldered onboard 4GB DDR3L SDRAM
Graphics Chipset	Integrated Intel® HD Graphic 400
Ethernet controller	1 x Intel® i210AT PCIe GbE controller
Audio	HD Link
BIOS	AMI BIOS
Serial Port	1 x UART port (TX, RX, RTX, CTS#)
Serial ATA	2 x Serial ATA ports w/ 600MB/s HDD transfer rate
USB	4 x USB 2.0 ports 2 x SuperSpeed ports
Graphics Interface	LCD: Dual Channels 24-bit LVDS, resolution up to 2048x1536
	1 x DisplayPort / HDMI selectable port
Expansion Bus	3 x PCIe x1, SDIO, I2C
Power Requirement	2A@5V with N3160 (Normal)
Operating Temp.	-20°C ~ 70°C (-4°F ~ 158°F)
Operating Humidity	10 ~ 95% @ 70°C (non-condensing)
Dimension (L x W)	70 x 70 mm (2.76" x 2.76")

## 1.4. Inside the Package

Before starting with the installation, make sure the following items are shipped. If any of the items is missing or appears damaged, contact your local dealer or distributor.



1 x EmQ-i2200 Qseven® CPU Module

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1 x Driver CD

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1 x Quick Installation Guide

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## 1.5. Ordering Information

EmQ-i2200-N3160-4G	Intel® Celeron N3160 Quad Core Qseven® R2.0 CPU Module w/4GB memory soldered on module
HS-2200-F1	Heat spreader, 70x65x8mm
HS-0000-W3	Universal evaluation Heatsink for Qseven® CPU module
PBQ-900L	Qseven R2.0 w/ EPIC form factor Carrier Board
CBK-06-900L-00	Cable kit 1 x USB cable 2 x COM cables 1 x SATA cable 1 x SATA power cable 1 x AUDIO cable

## 1.6. Driver Installation Note

The CPU board supports Windows 8.1. Find the necessary drivers on the CD that comes with your purchase. For different OS, the driver/utility installation may vary slightly, but generally they are similar.

Find the drivers on CD by the following paths:

### Windows 8.1

<b>Driver</b>	<b>Path</b>
Audio	\i220x\Audio
Chipset	\i220x\Chipset\Chipset_10.1.1.11_Public
Ethernet	\i220x\Ethernet
Graphics	\i220x\Graphic\IntelR Graphics Driver Production Version 15.40.14.64.4352
USB3.0	\i220x\USB3.0\win8.1\Intel(R) USB 3.0 eXtensible PV 1.0.0.42
Serial IO	\i220x\Serial IO\win8.1 64bit\SerialIO_BSW_x64
TXE	\i220X\TXE\win8.1\Installers

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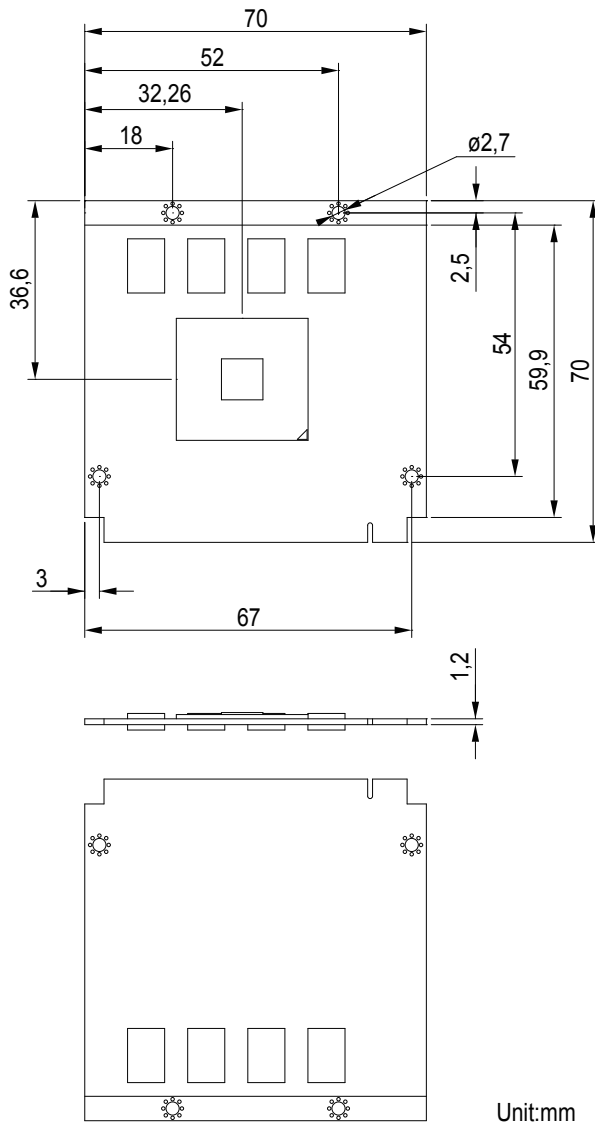


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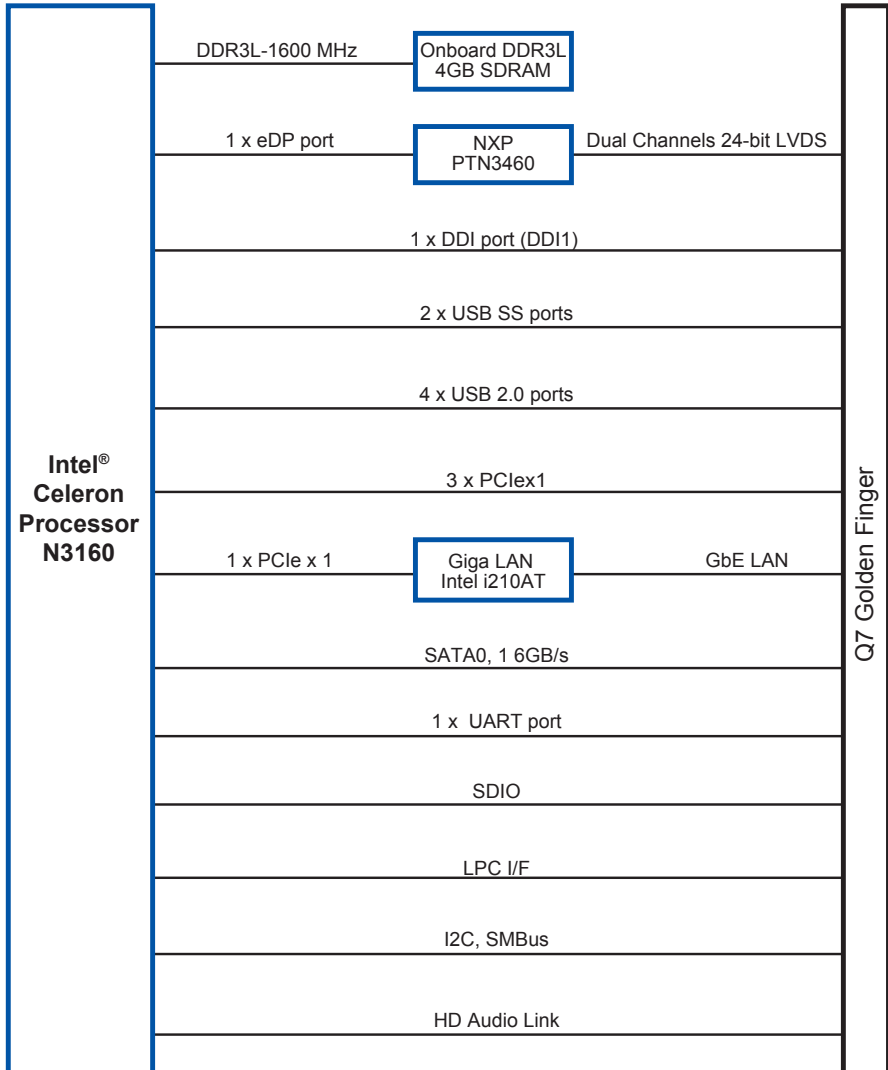
# Chapter 2

## Board Overview

## 2.1. Board Dimensions



## 2.2. Block Diagram



### 2.3. Connector Pin Definition

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	GND	2	GND	65	HDA_SDI	66	I2C_CLK
3	GBE_MDI3-	4	GBE_MDI2-	67	HDA_SDO	68	I2C_DAT
5	GBE_MDI3+	6	GBE_MDI2+	69	THRM#	70	WDTRIG#
7	GBE_LINK100#	8	GBE_LINK1000#	71	THRMTRIP#	72	WDOUT
9	GBE_MDI1-	10	GBE_MDI0-	73	GND	74	GND
11	GBE_MDI1+	12	GBE_MDI0+	75	USB_SSTX0-	76	USB_SSRX0-
13	GBE_LINK#	14	GBE_ACT#	77	USB_SSTX0+	78	USB_SSRX0-
15	GBE_CTREF (N/C)	16	SUS_S5#	79	USB_6_7_OC# (N/C)	80	USB_4_5_OC#
17	WAKE#	18	SUS_S3#	81	USB_SSTX1-	82	USB_SSRX1-
19	SUS_STAT#	20	PWRBTN#	83	USB_SSTX1+	84	USB_SSRX1+
21	SLP_BTN#	22	LID_BTN#	85	USB_2_3_OC#	86	USB_0_1_OC#
23	GND	24	GND	87	USB_P3-	88	USB_P2-
	KEY		KEY	89	USB_P3+	90	USB_P2+
25	GND	26	PWGIN	91	USB_CC(N/C)	92	USB_ID
27	BATLOW#	28	RSTBTN#	93	USB_P1-	94	USB_P0-
29	SATA0_TX+	30	SATA1_TX+	95	USB_P1+	96	USB_P0+
31	SATA0_TX-	32	SATA1_TX-	97	GND	98	GND
33	SATA_ACT#	34	GND	99	LVDS_A0+	100	LVDS_B0+
35	SATA0_RX+	36	SATA1_RX+	101	LVDS_A0-	102	LVDS_B0-
37	SATA0_RX-	38	SATA1_RX-	103	LVDS_A1+	104	LVDS_B1+
39	GND	40	GND	105	LVDS_A1-	106	LVDS_B1-
41	BIOS_DISABLE#	42	SDIO_CLK#	107	LVDS_A2+	108	LVDS_B2+
43	SDIO_CD#	44	SDIO_LED (N/C)	109	LVDS_A2-	110	LVDS_B2-
45	SDIO_CMD	46	SDIO_WP	111	LVDS_VDDEN	112	LVDS_BLEN
47	SDIO_PWR#	48	SDIO_DAT1	113	LVDS_A3+	114	LVDS_B3+
49	SDIO_DAT0	50	SDIO_DAT3	115	LVDS_A3-	116	LVDS_B3-
51	SDIO_DAT2	52	SDIO_DAT5 (N/C)	117	GND	118	GND
53	SDIO_DAT4 (N/C)	54	SDIO_DAT7 (N/C)	119	LVDS_A_CLK+	120	LVDS_B_CLK+
55	SDIO_DAT6 (N/C)	56	RSVD (N/C)	121	LVDS_A_CLK-	122	LVDS_B_CLK-
57	GND	58	GND	123	LVDS_BLT_CTRL	124	GP_1-Wire_Bus (N/C)
59	HDA_SYNC	60	SMB_CLK	125	LVDS_I2C_DAT	126	eDP0_HPD#/LVDS_BLC_DAT (N/C)
61	HDA_RST#	62	SMB_DAT	127	LVDS_I2C_CLK	128	eDP1_HPD#/LVDS_BLC_CLK (N/C)
63	HDA_BITCLK	64	SMB_ALERT#	129	CAN0_TX (N/C)	130	CAN0_RX (N/C)

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
131	DDIO_TX3+	132	RSVD (N/C)	197	GND	198	GND
133	DDIO_TX3-	134	RSVD (N/C)	199	SPI_MOSI	200	SPI_CS0#
135	GND	136	GND	201	SPI_MISO	202	SPI_CS1#
137	DDIO_TX1+	138	DDIO_AUX+	203	SPI_SCK	204	MFG_NC4 (N/C)
139	DDIO_TX1-	140	DDIO_AUX-	205	VCC_5V_SB	206	VCC_5V_SB
141	GND	142	GND	207	MFG_NC0 (N/C)	208	MFG_NC2 (N/C)
143	DDIO_TX2+	144	RSVD (N/C)	209	MFG_NC1 (N/C)	210	MFG_NC3 (N/C)
145	DDIO_TX2-	146	RSVD (N/C)	211	VCC	212	VCC
147	GND	148	GND	213	VCC	214	VCC
149	DDIO_TX0+	150	DDIO_DDC_DAT	215	VCC	216	VCC
151	DDIO_TX0-	152	DDIO_DDC_CLK	217	VCC	218	VCC
153	DP_HDMI_HPD#	154	RSVD (N/C)	219	VCC	220	VCC
155	PCIE_CLK_REF+	156	PCIE_WAKE#	221	VCC	222	VCC
157	PCIE_CLK_REF-	158	PCIE_RST#	223	VCC	224	VCC
159	GND	160	GND	225	VCC	226	VCC
161	PCIE3_TX+	162	PCIE3_RX+ (N/C)	227	VCC	228	VCC
163	PCIE3_TX-	164	PCIE3_RX- (N/C)	229	VCC	230	VCC
165	GND	166	GND				
167	PCIE2_TX+	168	PCIE2_RX+				
169	PCIE2_TX-	170	PCIE2_RX-				
171	UART0_TX	172	UART0_RTS				
173	PCIE1_TX+	174	PCIE1_RX+				
175	PCIE1_TX-	176	PCIE1_RX-				
177	UART0_RX	178	UART0_CTS#				
179	PCIE0_TX+	180	PCIE0_RX+				
181	PCIE0_TX-	182	PCIE0_RX-				
183	GND	184	GND				
185	LPC_AD0	186	LPC_AD1				
187	LPC_AD2	188	LPC_AD3				
189	LPC_CLK	190	LPC_FRAME#				
191	SERIRQ	192	LPC_LDRQ#				
193	VCC_RTC	194	SPKR				
195	FAN_TACHOIN (N/C)	196	FAN_PWMOUT				

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# Chapter 3

## BIOS

## BIOS

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The BIOS Setup utility is featured by AMI BIOS to configure the system settings stored in the system's BIOS ROM. AMI BIOS is activated once the computer powers on.

After entering the utility, use the left/right arrow keys to navigate between the top menus and use the down arrow key to access one.

Menu	Description
<b>Main</b>	See <a href="#">3.1 Main</a> on page <a href="#">15</a> .
<b>Advanced</b>	See <a href="#">3.2 Advanced</a> on page <a href="#">17</a> .
<b>Chipset</b>	See <a href="#">3.3 Chipset</a> on page <a href="#">30</a> .
<b>Boot</b>	See <a href="#">3.4 Security</a> on page <a href="#">36</a> .
<b>Security</b>	See <a href="#">3.5 Boot</a> on page <a href="#">37</a> .
<b>Save &amp; Exit</b>	See <a href="#">3.6 Save &amp; Exit</a> on page <a href="#">38</a> .

NOTE: For system stability and performance, this BIOS utility is constantly improved. The screenshots demonstrated and descriptions hereinafter are for reference only and may not exactly meet what is presented onscreen.



### 3.1 Main

The AMI BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS RAM of the system stores the Setup utility and configurations. When you turn on the computer, the AMI BIOS is immediately activated. To enter the BIOS SETUP UTILITY, press “Delete” once the power is turned on. When the computer is shut down, the battery on the motherboard supplies the power for BIOS RAM.

The **Main Setup** screen lists the following information:

Info Item	Description
<b>BIOS Name</b>	Delivers the Project name.
<b>BIOS Version</b>	Delivers the version of BIOS.
<b>Build Date and Time</b>	Delivers the date and time the BIOS Setup utility was made/updated.
<b>Access Level</b>	Delivers the level by which the BIOS Setup utility is being accessed at the moment.
<b>System Date</b>	Sets system date.

<b>System Time</b>	Sets system time.
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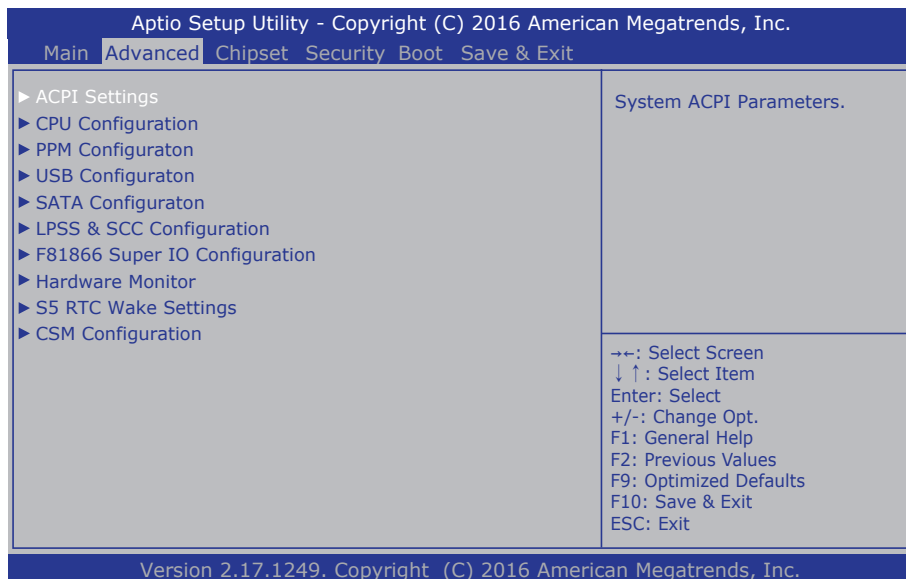
**Key Commands**

BIOS Setup Utility is mainly a key-based navigation interface. Please refer to the following key command instructions for navigation process.

Keystroke	Function
◀ ▶	Move to highlight a particular configuration screen from the top menu bar / Move to highlight items on the screen
▼ ▲	Move to highlight previous/next item
Enter	Select and access a setup item/field
Esc	On the Main Menu – Quit the setup and not save changes into CMOS (a message screen will display and ask you to select “OK” or “Cancel” for exiting and discarding changes. Use “←” and “→” to select and press “Enter” to confirm) On the Sub Menu – Exit current page and return to main menu
Page Up / +	Increase the numeric value on a selected setup item / make change
Page Down -	Decrease the numeric value on a selected setup item / make change
F1	Activate “General Help” screen
F0	Save the changes that have been made in the setup and exit. (a message screen will display and ask you to select “OK” or “Cancel” for exiting and saving changes. Use “←” and “→” to select and press “Enter” to confirm)

## 3.2 Advanced

The “Advanced” setting page provides you the options to configure the details of your hardware, such as ACPI, CPU, SATA, AMT, USB and Super IO.



Setting	Description
<b>ACPI Settings</b>	See Section <a href="#">3.2.1 ACPI Settings on the page 18</a>
<b>CPU Configuration</b>	See Section <a href="#">3.2.2 CPU Configuration on the page 19</a>
<b>PPM Configuration</b>	See Section <a href="#">3.2.3 PPM Configuration on the page 20</a>
<b>USB Configuration</b>	See Section <a href="#">3.2.4 USB Configuration on the page 21</a>
<b>SATA Configuration</b>	See Section <a href="#">3.2.5 SATA Configuration on the page 23</a>
<b>LPSS &amp; SCC Configuration</b>	See Section <a href="#">3.2.6 LPSS &amp; SCC Configuration on the page 24</a>
<b>F81866 Super IO Configuration</b>	See Section <a href="#">3.2.7 F81899 Super IO Configuration on the page 25</a>
<b>Hardware Monitor</b>	See Section <a href="#">3.2.8 Hardware Monitor on the page 26</a>
<b>S5 RTC Wake Setting</b>	See Section <a href="#">3.2.9 S5 RTC Wake Settings on the page 27</a>
<b>CSM Configuration</b>	See Section <a href="#">3.2.10 CSM Configuration on the page 28</a>

### 3.2.1 ACPI Settings

Aptio Setup Utility - Copyright (C) 2016 American Megatrends, Inc.  
 Advanced

ACPI Settings		Enables or Disables System ability to hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
Enable Hibernation	[Enabled]	
ACPI Sleep State	[S3 (Suspend to RAM)]	

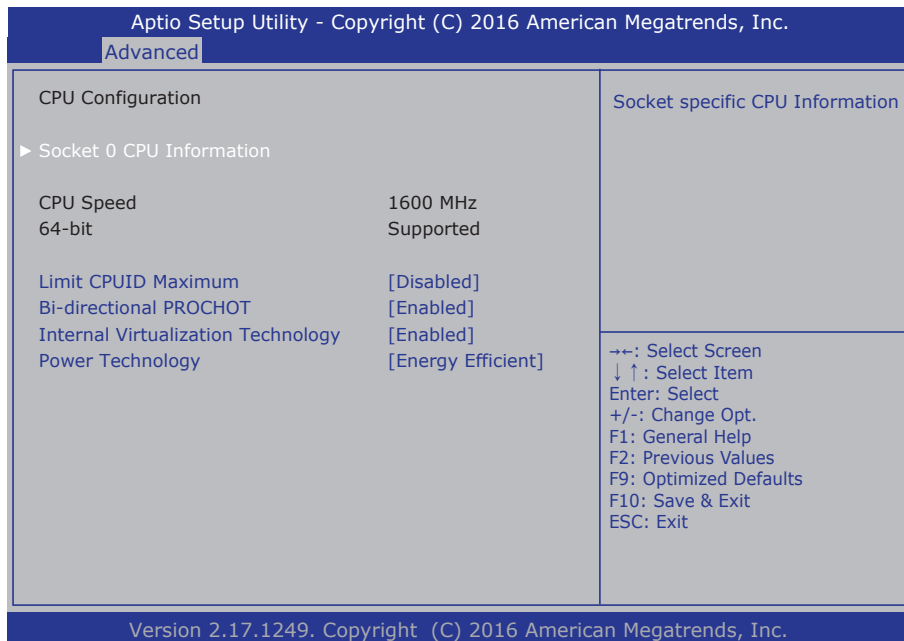
→+: Select Screen  
 ↓↑: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F9: Optimized Defaults  
 F10: Save and Exit  
 ESC: Exit

Version 2.17.1249. Copyright (C) 2016 American Megatrendes, Inc.

Setting	Description
Enable Hibernation	<b>Enables</b> (default) or <b>Disables</b> System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	Select ACPI sleep state the system will enter when the SUSPEND button is pressed. ► Options: <b>Suspend Disabled</b> and <b>S3 (Suspend to RAM)</b> (default).

### 3.2.2 CPU Configuration

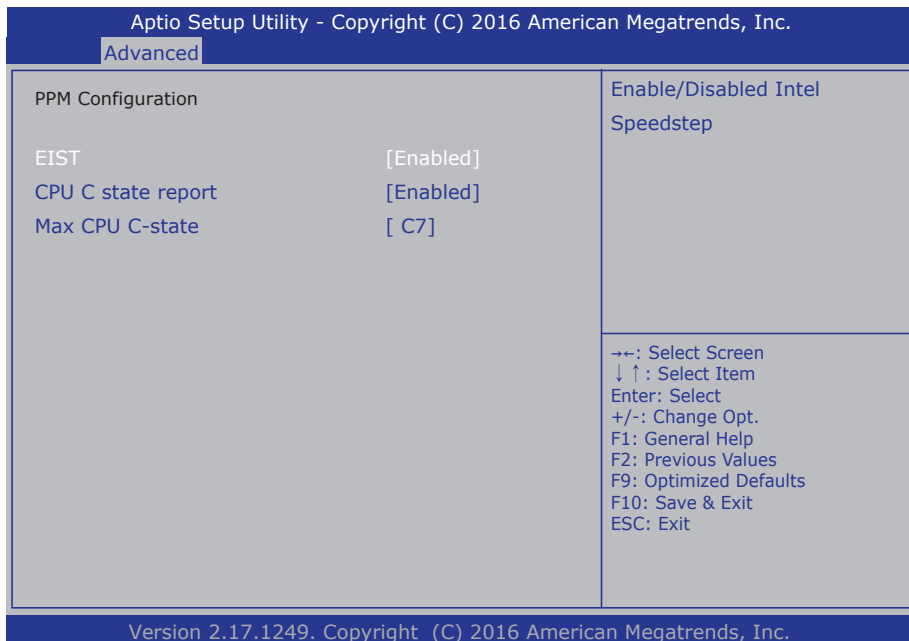
Access this submenu to configure the CPU features.



Setting	Description
Socket 0 CPU Information	Display Socket specific CPU Information.
Limit CPUID Maximum	Enables/disables the maximum CPUID value limit. Enable this item to prevent the system from “rebooting” when trying to install Windows XP. ▶ <b>Disabled</b> is the default.
Bi-Directional PROCHOT	When a processor thermal sensor trips(either core), the PROCHOT# will be driven. If Bi-Directional is enable, external agents can drive PROCHOT# to throttle.
Intel Virtualization Technology	Enables/disables the Intel Virtualization Technology. When enabled, a VMM can utilize the additional hardware capabilities provided by Vendor Pool Technology. ▶ <b>Enabled</b> is the default.
Power Technology	Enable the power management features. ▶ Options: <b>Disabled</b> , <b>Energy Efficient</b> (default), <b>Custom</b>

### 3.2.3 PPM Configuration

Access this submenu to setup the PPM Configuration.



Setting	Description
EIST	Enable/Disable Intel SpeedStep ▶ <b>Enabled</b> is the default.
CPU C state Report	Enable/Disable CPU C state report to OS ▶ <b>Enabled</b> is the default.
Max CPU C-state	This option controls Max C state that the processor will support ▶ Options: <b>C7</b> (default), <b>C6</b> , <b>C1</b>

### 3.2.4 USB Configuration

Select this submenu to view the status of the USB ports and configure USB features.

Aptio Setup Utility - Copyright (C) 2016 American Megatrends, Inc.	
Main <b>Advanced</b> Chipset Boot Security Save & Exit	
USB Configuration	Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available. only for EFI applications.
USB Module Version	13
USB Controllers:	
1 XHCI	
USB Devices:	
1 Keyboard, 1 Mouse	
Legacy USB Support	[Enabled]
XHCI Hand-off	[Enabled]
USB Mass Storage Driver Support	[Enabled]
USB hardware delays and time-outs:	
USB transfer time-out	[20 sec]
Device reset time-out	[20 sec]
Device power-up delay	[Auto]
	→+: Select Screen ↓ ↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.17.1249. Copyright (C) 2016 American Megatrends, Inc.	

The featured settings and delivered info are:

Setting / Info	Description
Legacy USB Support	Enables/disables legacy USB support. <ul style="list-style-type: none"> <li>▶ Options available are <b>Enabled</b> (default), <b>Disabled</b> and <b>Auto</b>.</li> <li>▶ Select <b>Auto</b> to disable legacy support if no USB device are connected.</li> <li>▶ Select <b>Disabled</b> to keep USB devices available only for EFI applications.</li> </ul>
XHCI Hand-off	Enables/disables a workaround for the operating systems that have no XHCI hand-off support <ul style="list-style-type: none"> <li>▶ <b>Enabled</b> is the default.</li> </ul>

USB Mass Storage Driver Support	Enables/disables the support for USB mass storage driver. ▶ <b>Enabled</b> is the default.
USB transfer time-out	The time-out value for Control, Bulk and Interrupt transfers. ▶ Options: <b>1/5/10/20 sec</b> (default)
Device reset time-out	USB mass storage device Start Unit command time-out. ▶ Options: <b>10/20</b> (default)/ <b>30/40 sec</b>
Device power-up delay	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor. ▶ Options: <b>Auto</b> (default), <b>Manual</b>



### 3.2.5 SATA Configuration

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Advanced	
SATA Configuration	Enable/Disable SATA Device.
SATA Controller	[Enabled]
SATA Interface Speed	[Gen3]
SATA Port0 MRSAJ5D016GC12 (16.0GB) Port 0	[Enabled]
SATA Port1 Not Present Port 1	[Enabled]
	→+: Select Screen ↓↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F9: Optimized Defaults F10: Save and Exit ESC: Exit
Version 2.17.1249. Copyright (C) 2016 American Megatrendes, Inc.	

Setting	Description
SATA Controller	Enables/disables SATA device. ▶ <b>Enabled</b> is the default.
SATA Interface Speed	Configures the maximum speed of SATA controller. ▶ Options available are <b>Gen1</b> , <b>Gen2</b> and <b>Gen3(default)</b> .
Port 0/1	Enables/disables SATA Port 0/1. ▶ <b>Enabled</b> is the default.

### 3.2.6 LPSS & SCC Configuration

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Advanced

<p>SCC Configuration                  SCC SD Card Support (D18:F0)      [PCI Mode]</p> <p>LPSS Configuration                  LPSS I2C #1(D24: F1)              [ACPI Mode]                  Runtime D3 Support                  [Enabled]</p>	<p>SCC SD Card Support                  Enable\Disable.</p>           <p>→←: Select Screen                  ↓↑: Select Item                  Enter: Select                  +/-: Change Opt.                  F1: General Help                  F2: Previous Values                  F9: Optimized Defaults                  F10: Save &amp; Exit                  ESC: Exit</p>
--	---

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Setting	Description
SCC SD Card Support (D18:F0)	Set the mode of SCC SD Card Support mode. ► Options: <b>ACPI mode/PCI mode</b> (default)/ <b>Disabled</b>
LPSS I2C #1 (D24:F1)	Set the mode of LPSS I2C #1. ► Options: <b>ACPI mode</b> (default)/ <b>PCI mode/Disabled</b>
Runtime D3 Support	Enable or disable Runtime D3 Support. ► <b>Enabled</b> is the default.

### 3.2.7 F81899 Super IO Configuration

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Advanced

<p>Super IO Configuration</p> <p>Super IO Chip F81866</p> <ul style="list-style-type: none"> <li>▶ Serial Port 2 Configuration</li> <li>▶ Serial Port 3 Configuration</li> <li>▶ Serial Port 4 Configuration</li> <li>▶ Serial Port 5 Configuration</li> </ul>	<p>Set Parameters of Serial Port 2 (COM2)</p> <hr/> <p>→+: Select Screen          ↓↑: Select Item          Enter: Select          +/-: Change Opt.          F1: General Help          F2: Previous Values          F9: Optimized Defaults          F10: Save and Exit          ESC: Exit</p>
--	--

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Setting	Description						
Serial Port 2/4/5 Configuration	<p>Set the Parameters of Serial Port 2/4/5</p> <table border="1"> <tr> <td><b>Serial Port</b></td> <td>Enable or disable Serial Port. ▶ <b>Enabled</b> is the default.</td> </tr> <tr> <td><b>Change Setting</b></td> <td>Select an optimal setting for Super IO device.</td> </tr> </table>	<b>Serial Port</b>	Enable or disable Serial Port. ▶ <b>Enabled</b> is the default.	<b>Change Setting</b>	Select an optimal setting for Super IO device.		
	<b>Serial Port</b>	Enable or disable Serial Port. ▶ <b>Enabled</b> is the default.					
<b>Change Setting</b>	Select an optimal setting for Super IO device.						
Serial Port 3 Configuration	<p>Set the Parameters of Serial Port 3</p> <table border="1"> <tr> <td><b>Serial Port</b></td> <td>Enable or disable Serial Port. ▶ <b>Enabled</b> is the default.</td> </tr> <tr> <td><b>Change Setting</b></td> <td>Select an optimal setting for Super IO device.</td> </tr> <tr> <td><b>RS485 AutoFlow</b></td> <td>Enable or disable RS485 AutoFlow. ▶ <b>Disabled</b> is the default.</td> </tr> </table>	<b>Serial Port</b>	Enable or disable Serial Port. ▶ <b>Enabled</b> is the default.	<b>Change Setting</b>	Select an optimal setting for Super IO device.	<b>RS485 AutoFlow</b>	Enable or disable RS485 AutoFlow. ▶ <b>Disabled</b> is the default.
	<b>Serial Port</b>	Enable or disable Serial Port. ▶ <b>Enabled</b> is the default.					
	<b>Change Setting</b>	Select an optimal setting for Super IO device.					
<b>RS485 AutoFlow</b>	Enable or disable RS485 AutoFlow. ▶ <b>Disabled</b> is the default.						

### 3.2.8 Hardware Monitor

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Advanced

Pc Health Status	
CPU Temperature	: +50 °C
System Temperature	: +43 °C
Fan1 Speed	: N/A
+3.3V	: +3.344 V
+V5A	: +4.961 V
+V5S	: +4.961 V
+V12S	: +11.792 V
VCC3V	: +3.344 V
VS3V	: +3.344 V
VS5V	: +5.012 V
VBAT	: +3.027 V

→+: Select Screen  
↓↑: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save and Exit  
ESC: Exit

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### 3.2.9 S5 RTC Wake Settings

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Advanced

Wake system from S5	[Disabled]	Enables or disables system wake on alarm event. When enabled, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s)
		+-: Select Screen ↓↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F9: Optimized Defaults F10: Save and Exit ESC: Exit

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The featured submenus are:

Setting	Description						
Wake system from S5	Select System wake on alarm Event ▶ Options: <b>Disabled(default)/Fixed Time/Dynamic Time</b>						
Wake System with Fixed Time	Sets if to awake the system at a defined moment. <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <tr> <td style="width: 20%; background-color: #d9ead3;"><b>Wake up hour</b></td> <td>                             Defines the (hour) time to awake the system.                              ▶ 0 to 23 configurable.                         </td> </tr> <tr> <td style="background-color: #d9ead3;"><b>Wake up Minute</b></td> <td>                             Defines the (minute) time to awake the system.                              ▶ 0 to 59 configurable.                         </td> </tr> <tr> <td style="background-color: #d9ead3;"><b>Wake up second</b></td> <td>                             Defines the (second) time to awake the system.                              ▶ 0 to 59 configurable.                         </td> </tr> </table>	<b>Wake up hour</b>	Defines the (hour) time to awake the system. ▶ 0 to 23 configurable.	<b>Wake up Minute</b>	Defines the (minute) time to awake the system. ▶ 0 to 59 configurable.	<b>Wake up second</b>	Defines the (second) time to awake the system. ▶ 0 to 59 configurable.
<b>Wake up hour</b>	Defines the (hour) time to awake the system. ▶ 0 to 23 configurable.						
<b>Wake up Minute</b>	Defines the (minute) time to awake the system. ▶ 0 to 59 configurable.						
<b>Wake up second</b>	Defines the (second) time to awake the system. ▶ 0 to 59 configurable.						
Wake System with Dynamic Time	Sets if to awake the system some time in the future. <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <tr> <td style="width: 20%; background-color: #d9ead3;"><b>Wake up minute increase</b></td> <td>                             Defines how long from now to awake the system.                              ▶ 1 to 5 minutes configurable.                         </td> </tr> </table>	<b>Wake up minute increase</b>	Defines how long from now to awake the system. ▶ 1 to 5 minutes configurable.				
<b>Wake up minute increase</b>	Defines how long from now to awake the system. ▶ 1 to 5 minutes configurable.						

### 3.2.10 CSM Configuration

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Advanced

Compatibility Support Module Configuration		Enable/Disable CSM Support.
CSM Support	[Enabled]	
CSM16 Module Version	07.79	
GateA20 Active	[Upon Request]	
Option ROM Message	[Force BIOS]	
INT19 Trap Response	[Immediate]	
Boot option filter	[UEFI and Legacy]	
Option ROM execution		++: Select Screen ↓↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F9: Optimized Defaults F10: Save and Exit ESC: Exit
Network	[Do not launch]	
Storage	[Legacy]	
Video	[Legacy]	
Other PCI devices	[Legacy]	

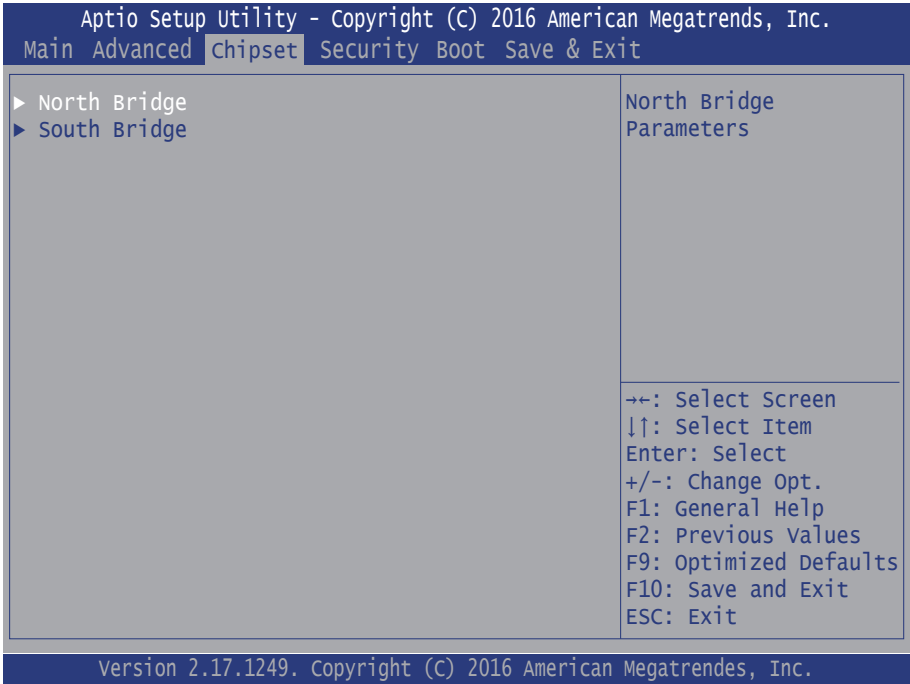
Version 2.17.1249. Copyright (C) 2016 American Megatrends, Inc.

The featured submenus are:

Setting	Description
CSM Support	Enable and Disable CSM Support ▶ <b>Enabled</b> is the default.
GateA20 Active	Select setting for GateA20. ▶ Options: <b>Upon Request</b> (default) , and <b>Always</b> .
Option ROM Message	Select display mode for Option ROM. ▶ Options: <b>Force BIOS</b> (default), and <b>Keep Current</b> .
INT19 Trap Response	BIOS reaction on INT19 trapping by Option ROM: ▶ Options: <b>Immediate</b> (default) and <b>Postponed</b> Immediate: Execute the trap right away Postponed: Execute the trap during legacy boot.
Boot option filter	Controls Legacy/UEFI ROMs priority. ▶ Options: <b>UEFI and Legacy</b> (default), <b>Legacy only</b> and <b>UEFI only</b>

Network	Control the execution of UEFI and Legacy PXE OpROM. ▶ Options: <b>Do not launch, UEFI and Legacy.</b>
Storage	Control the execution of UEFI and Legacy Storage OpROM. ▶ Options: <b>Do not launch, UEFI and Legacy.</b>
Video	Control the execution of UEFI and Legacy Video OpROM. ▶ Options: <b>Do not launch, UEFI and Legacy.</b>
Other PCI device	Set the OpROM execution policy for devices other than Network, Storage, or Video. ▶ Options: <b>Do not launch, UEFI and Legacy.</b>

### 3.3 Chipset



Setting	Description
North Bridge	See Section <a href="#">3.3.1 North Bridge on the page 31</a>
South Bridge	See Section <a href="#">3.3.2 South Bridge on the page 34</a>



### 3.3.1 North Bridge

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Chipset

<ul style="list-style-type: none"> <li>▶ Intel IGD Configuration</li> <li>▶ LCD Control</li> </ul> <p>Memory Information</p> <p>Total Memory                      4096 MB (LPDDR3)</p>	<p>Config Intel IGD Settings.</p>
	<p>→+: Select Screen          ↓↑: Select Item          Enter: Select          +/-: Change Opt.          F1: General Help          F2: Previous Values          F9: Optimized Defaults          F10: Save and Exit          ESC: Exit</p>

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The featured settings are:

Setting/Submenu	Description
Intel IGD Configuration	Configures the Intel IGD Configuration.
LCD Control	Configures the LCD Control.

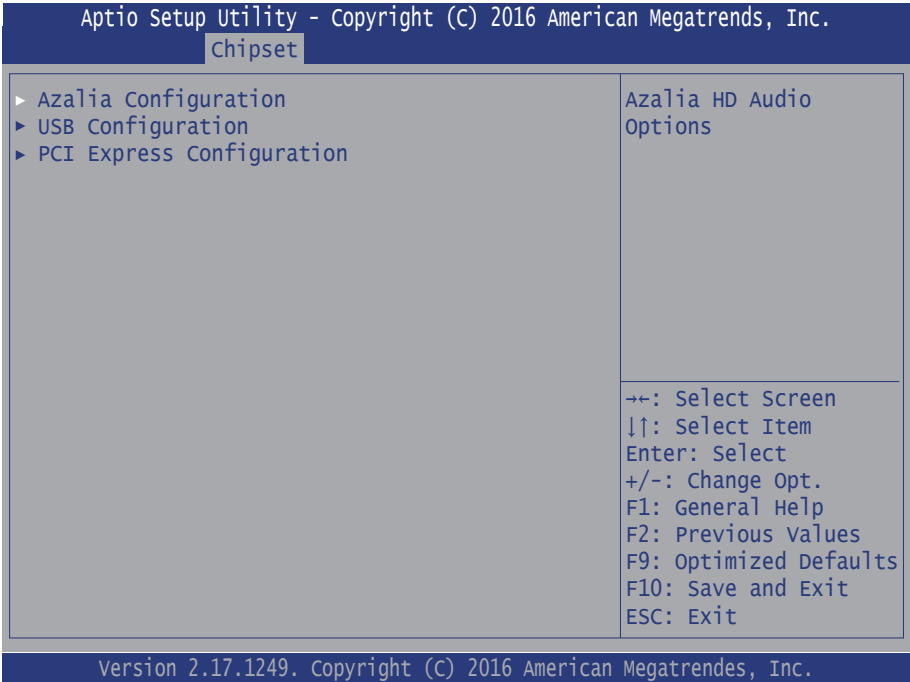
### 3.3.1.1 Intel IGD Configuration

Setting	Description
GOP Driver	Enable and Disable GOP Driver ▶ <b>Enabled</b> is the default.
Integrated Graphics Device	Enable and Disable IGD. ▶ <b>Enabled</b> is the default.
IGD Turbo	Select the IGD Turbo. If Auto selected, IGD Turbo will only be enabled when SOC steeping is B0 or above. ▶ Options: <b>Auto</b> (default), <b>Enabled</b> and <b>Disabled</b> .
DVMT Pre-Allocated	Select the DVMT 5.0 Pre-allocated (Fixed) Graphic Memory size used by the Internal Graphic Device. ▶ Options: 32M is the default.
DVMT total Gfx Mem	Select the DVMT 5.0 Total Graphic Memory size used by the Internal Graphic Device. ▶ Options: <b>256MB</b> (default), <b>128MB</b> and <b>Max</b> .
Aperture Size	Select the Aperture Size. ▶ Options: <b>256MB</b> (default), <b>128MB</b> and <b>512MB</b> .
GTT Size	Select the GTT Size. ▶ Options: <b>4MB</b> (default), <b>2MB</b> and <b>8MB</b> .

### 3.3.1.2 LCD Control

Setting	Description
Boot Display	Select the video device which will be activated during POST. This has no effect if external graphics present. Secondary boot display selection will appear based on your selection. VGA modes will be supported only on primary display. ▶ Options: <b>Auto</b> (default), <b>DVI</b> and <b>LCD</b> .
LCD Panel Type	Select LCD panel used by Internal Graphics Device by selecting the appropriate setup item. Default is <b>1024x768</b> .
Panel Scaling	Select LCD panel scaling option used by the Internal Graphics Device. ▶ Options: <b>Auto</b> (default), <b>Off</b> , and <b>Force Scaling</b>
Backlight Control	Select Light Control setting ▶ Options: <b>PWM Normal</b> (default), <b>PWM Inverted</b> , <b>GMBus Inverted</b> , and <b>GMBus Normal</b>
LVDS Channel Type	Select single and dual channel. ▶ Options: <b>Dual</b> , and <b>Single</b> (default)
LVDS Panel Color Format	Select LVDS color display mode. ▶ Options: <b>18-BIT</b> (default), <b>24-BIT</b>

### 3.3.2 South Bridge



The featured settings are:

Setting	Description
Azalia Configuration	Configures the Azalia Configuration
USB Configuration	Configures the USB Configuration
PCI Express Configuration	Configures the PCI Express Configuration

### 3.3.2.1 SB HD Azalia Configuration

Item	Description
Audio Controller	▶ Options: <b>Disabled</b> and <b>Enabled</b> (default).

### 3.3.2.2 USB Configuration

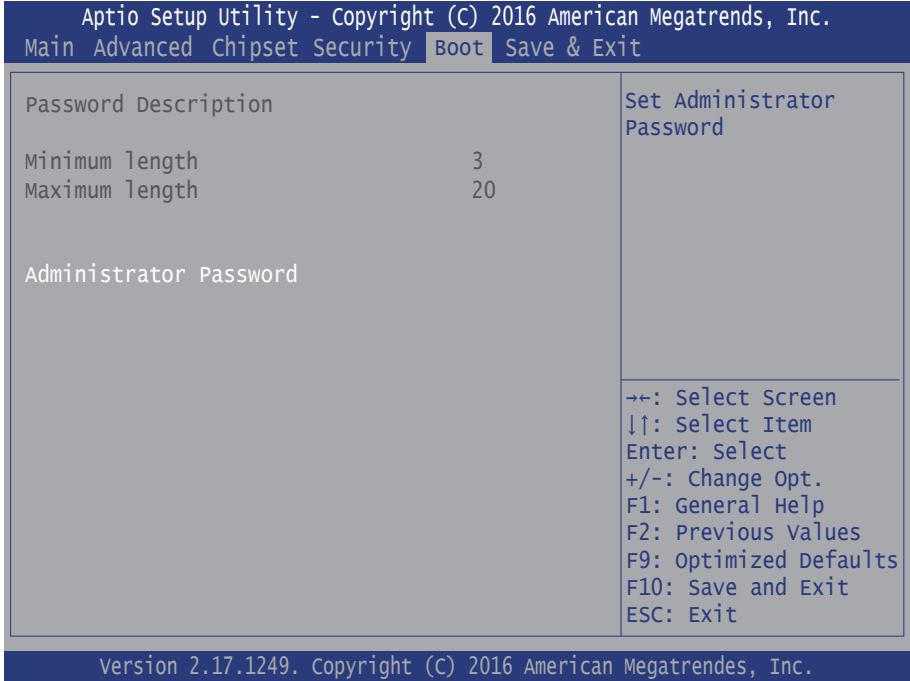
Item	Description
XHCI Mode	<b>Enable</b> (default) or <b>Disable</b> XHCI Mode.
USB Port 0/1/2/3/4	<b>Enable</b> (default) or <b>Disable</b> USB Port 0/1/2/3/4.
SSIC Support Enable	<b>Enable</b> or <b>Disable</b> (default) SSIC Support
SSIC Port 1/2	<b>Enable</b> or <b>Disable</b> (default) SSIC Port 1/2
HSIC Port 1/2	<b>Enable</b> (default) or <b>Disable</b> HSIC Port 1/2

### 3.3.2.3 PCI Express Configuration

Item	Description
PCI Express Root Port 1/2/3/4	Control the PCI Express Root Port. ▶ <b>Enabled</b> is the default.
ASPM	PCI Express Active State Power Management settings. ▶ Options: <b>Disabled</b> (default), <b>L0s</b> , <b>L1</b> , <b>L0sL1</b> and <b>Auto</b>
PCIe Speed	Configure PCIe Speed. CHV A1 always with Gen1 speed. ▶ Options: <b>Auto</b> (default), <b>Gen 2</b> and <b>Gen 1</b>

### 3.4 Security

The **Security** menu sets up the administrator password.



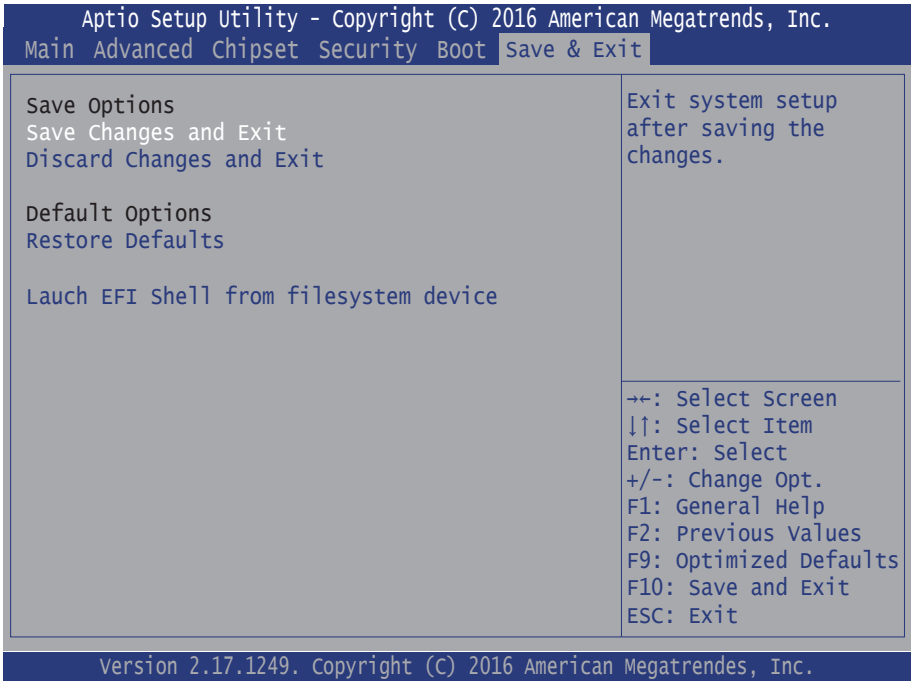
Setting	Description
Administrator Password	<p>To set up an administrator password:</p> <ol style="list-style-type: none"> <li>1. Select <b>Administrator Password</b>. The screen then pops up an <b>Create New Password</b> dialog.</li> <li>2. Enter your desired password that is no less than 3 characters and no more than 20 characters.</li> <li>3. Hit [Enter] key to submit.</li> </ol>

### 3.5 Boot

Aptio Setup Utility - Copyright (C) 2016 American Megatrends, Inc.		
Main Advanced Chipset Security <b>Boot</b> Save & Exit		
Boot Configuration		Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Setup Prompt Timeout	1	
Bootup NumLock State	[On]	
Quiet Boot	[Disabled]	
Boot Option Priorities		
Boot Option#1	[P0: MRSAJ5D016GC..]	
Boot Option#2	[ ]	
Hard Drive BBS Priorites		
		→←: Select Screen ↓↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F9: Optimized Defaults F10: Save and Exit ESC: Exit
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Setting	Description
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Boot NumLock State	Select the keyboard NumLock state. ► Options: <b>On</b> (default) and <b>Off</b> .
Quiet Boot	<b>Enable</b> (default) or <b>Disable</b> Quiet Boot option.
Boot Option Priorities	Sets the boot priority among the available device types.

### 3.6 Save & Exit



Setting	Description
Save Changes and Exit	Exit system setup after saving the changes. ▶ Enter the item and then a dialog box pops up: <b>Save configuration and exit? (Yes/ No)</b>
Discard Changes and Exit	Exit system setup without saving the changes. ▶ Enter the item and then a dialog box pops up: <b>Quit without saving? (Yes/ No)</b>
Restore Defaults	Restore/Load Default values for all the setup options. ▶ Enter the item and then a dialog box pops up: <b>Load Optimized Defaults? (Yes/ No)</b>
Launch EFI Shell from filesystem device	Attempts to launch EFI shell application (Shell.efi) from one of the available filesystem devices.



---

# Appendices

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## Appendix A. I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device.

The following table lists the I/O port addresses used.

Address	Device Description
0x000003F8-0x000003FF	Communications Port (CON1)
0x000002F8-0x000002FF	Communications Port (COM1)
0x000003E8-0x000003EF	Communications Port (COM2)
0x000002E8-0x000002EF	Communications Port (COM3)
0x000002F0-0x000002F7	Communications Port (COM4)
0x0000D000-0x0000D01F	Ethernet Controller
0x0000E000-0x0000E01F	Ethernet Controller
0x00000060-0x00000060	Microsoft PS/2 Mouse
0x00000064-0x00000064	Microsoft PS/2 Mouse
0x00000070-0x00000077	Motherboard resources
0x00000A00-0x00000A0F	Motherboard resources
0x00000A10-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000080-0x0000008F	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000B2-0x000000B3	Motherboard resources
0x00000400-0x0000047F	Motherboard resources
0x00000500-0x000005FE	Motherboard resources
0x00000CF8-0x00000CFF	PCI bus
0x00000D00-0x0000FFFF	PCI bus
0x0000D000-0x0000D01F	PCI Express standard Root Port
0x0000E000-0x0000E01F	PCI Express standard Root Port
0x00000020-0x00000021	Programmable interrupt controller
0x000000A0-0x000000A1	Programmable interrupt controller
0x000004D0-0x000004D1	Programmable interrupt controller
0x0000F040-0x0000F05F	SM Bus Controller
0x0000F060-0x0000F07F	Standard AHCI 1.0 Serial ATA controller
0x00000060-0x00000060	Standard PS/2 Keyboard
0x00000064-0x00000064	Standard PS/2 Keyboard
0x0000F000-0x0000F03F	Standard VGA Graphics Adapter
0x000003B0-0x000003BB	Standard VGA Graphics Adapter
0x000003C0-0x000003DF	Standard VGA Graphics Adapter
0x00000070-0x00000071	System CMOS/real time clock
0x00000040-0x00000043	System timer
0x00000050-0x00000053	System timer

## Appendix B. Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Level	Function
<b>IRQ0</b>	System timer
<b>IRQ1</b>	Standard PS/2 Keyboard
<b>IRQ3</b>	Communications Port (COM1)
<b>IRQ4</b>	Communications Port (COM1)
<b>IRQ5</b>	Ethernet Controller
<b>IRQ5</b>	Ethernet Controller
<b>IRQ5</b>	SM Bus Controller
<b>IRQ5</b>	PCI Encryption/Decryption Controller
<b>IRQ7</b>	Communications Port (COM4)
<b>IRQ10</b>	Communications Port (COM3)
<b>IRQ11</b>	Communications Port (COM2)
<b>IRQ12</b>	Microsoft PS/2 Mouse
<b>IRQ18</b>	SDA Standard Compliant SD Host Controller
<b>IRQ19</b>	Standard AHCI 1.0 Serial ATA Controller
<b>IRQ22</b>	High Definition Audio Controller

## Appendix C. BIOS Memory Map

Address	Device Description
0xFF000000-0xFFFFFFFF	Intel(R) 82802 Firmware Hub Device
0x81200000-0x8127FFFF	Ethernet Controller
0x81200000-0x8127FFFF	PCI Express standard Root Port
0x81280000-0x81283FFF	Ethernet Controller
0x8141C000-0x8141C7FF	Standard AHCI 1.0 Serial ATA Controller
0xFED80000-0xFED87FFF	Motherboard resources
0x81300000-0x8137FFFF	Ethernet Controller
0x81300000-0x8137FFFF	PCI Express standard Root Port
0x81380000-0x81383FFF	Ethernet Controller
0x80000000-0x80FFFFFF	Standard VGA Graphics Adapter
0x80000000-0x80FFFFFF	PCI bus
0x90000000-0x9FFFFFFF	Standard VGA Graphics Adapter
0xA0000-0xBFFFF	Standard VGA Graphics Adapter
0xA0000-0xBFFFF	PCI bus
0x81400000-0x8140FFFF	Intel(R) USB 3.0 extensible host controller
0x81410000-0x81413FFF	High Definition Audio Controller
0x81418000-0x8141801F	SM Bus Controller
0xC0000-0xDFFFF	PCI bus
0xE0000-0xFFFFF	PCI bus
0x8141D000-0x8141DFFF	SDA Standard Compliant SD Host Controller
0xE0000000-0xEFFFFFFF	Motherboard resources
0xFEAA0000-0xFEFFFFFF	Motherboard resources
0xFED01000-0xFED01FFF	Motherboard resources
0xFED03000-0xFED03FFF	Motherboard resources
0xFED06000-0xFED06FFF	Motherboard resources
0xFED08000-0xFED09FFF	Motherboard resources
0xFED1C000-0xFED1CFFF	Motherboard resources
0xFEE00000-0xFEEFFFFFF	Motherboard resources
0x8141B000-0x8141BFFF	Motherboard resources
0x81419000-0x81419FFF	Motherboard resources
0x81100000-0x811FFFFFF	PCI Encryption/Decryption Controller
0x81000000-0x810FFFFFF	PCI Encryption/Decryption Controller

## Appendix D: Watchdog Timer (WDT) Setting

WDT is widely used for industry application to monitor the activity of CPU. Application software depends on its requirement to trigger WDT with adequate timer setting. Before WDT time out, the functional normal system will reload the WDT. The WDT never time out for a normal system. The WDT will not be reloaded by an abnormal system, then WDT will time out and reset the system automatically to avoid abnormal operation.

This board supports 255 levels watchdog timer by software programming I/O ports. Below are the source codes written in C, please take them as WDT application example.

```
#include "math.h"
#include "stdio.h"
#include "dos.h"

#define DELAY_TIME 10

#define _SMBBA 0xF040 /* SMBus Base Address
*/
#define _SMBSA 0x6E /* SMBus Slave
Address , 75111R's Add = 6Eh or 9Ch */

unsigned char DIO_Set(unsigned char oMode, unsigned char oData);
unsigned char SMB_Byte_READ(int SMPORT, int DeviceID, int iREG_INDEX);
void SMB_Byte_WRITE(int SMPORT, int DeviceID, int oREG_INDEX, int oREG_DATA);

void main()
{
    WDT_Start(10);

    while(1)
    {
        iCount = WDT_Count();
        printf("\r Counts : %d ",iCount);

        delay(1000);
    }
}

void WDT_Start(int iCount)
{
    int iData;

    /* Configuration and function select Register - Enable WDTOUT2# output */
    iData = SMB_Byte_READ(SMB_PORT_AD,SMB_DEVICE_ADD,0x03);
    iData = iData | 0x03;
    SMB_Byte_WRITE(SMB_PORT_AD,SMB_DEVICE_ADD,0x03,iData);
    delay(DELAY_TIME);

    /* Watchdog Timer Range Register */
    SMB_Byte_WRITE(SMB_PORT_AD,SMB_DEVICE_ADD,0x37,iCount);
}
```

## Appendix

---

```
    delay(DELAY_TIME);

    /* Watchdog Timer Control Register */
    SMB_Byte_WRITE(SMB_PORT_AD, SMB_DEVICE_ADD, 0x36, 0x72);
}

int WDT_Count(void)
{
    int iData;

    /* Watchdog Timer Range Register */
    iData = SMB_Byte_READ(SMB_PORT_AD, SMB_DEVICE_ADD, 0x37);

    return iData;
}

void WDT_Clear(int iCount)
{
    /* Watchdog Timer Range Register */
    SMB_Byte_WRITE(SMB_PORT_AD, SMB_DEVICE_ADD, 0x37, iCount);
}

void WDT_Stop(void)
{
    /* Watchdog Timer Control Register */
    SMB_Byte_WRITE(SMB_PORT_AD, SMB_DEVICE_ADD, 0x36, 0x52);
}
```